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(Very High Speed Integrated Circuits), a 3G mobile telephony communications protocol in; High Speed Telegraphy High Speed Digital Design:

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CMOS circuits for high speed serial data communication Xiaoyu Xi [21] David Johns & Ken Martin, Analog Integrated Circuit Design, John Wiley & Sons Inc.

## **Course descriptions - college of engineering**

concepts for modern communication systems; topics High-Speed Links Circuits and Systems. for CMOS RFIC design. Prerequisites: ECEN 322 and

## **High- speed optical transceivers: integrated**

High-speed Optical Transceivers: Integrated Circuit Design And Optical Device Techniques (Selected Topics in Electronics and Sstems) [Huazhong Yang Yuyu Liu] on Amazon

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Genre/Form: Electronic books: Additional Physical Format: Print version: Design of high-speed communication circuits. New Jersey : World Scientific, 2006

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H.-S. Lee & M.H. Perrott MIT OCW LNA Design Example In our previous design example, we picked the Q for the minimum possible noise factor:  $Q=1.4$

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### **High- speed digital circuit design - academia.edu**

High-Speed Digital Circuit Design. People 47. Documents 8. Electrical Engineering, High-Speed Digital Circuit Design, RF communication, Microstrip, and Attenuation

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### **Digital electronics - wikipedia, the free**

4.1 Structure of digital systems. 4.4.4 Speed; 4.5 Logic families; Representations are crucial to an engineer's design of digital circuits.

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